PACKAGING STRUCTURE OF IMAGE SENSOR AND METHOD FOR PACKAGING THE SAME

BACKGROUND OF THE INVENTION

Field of the invention

The invention relates to a packaging structure of image sensors and method for packaging the same, in particular, to a packaging structure in which a substrate for carrying an image sensing chip is formed using plastic materials and the substrate includes metallic pins, thereby lowering the manufacturing costs.

Description of the related art

In typical image sensors, in order to achieve the demands for high heat dissipating efficiency and high sealing property, the substrate for carrying the image sensing chip is formed from ceramic materials. A plurality of \Box -shaped pins connected to each other are formed on the top and bottom surfaces of the substrate extending from the periphery thereof. The image sensing chip is placed on the substrate. Then, the image sensing chip is electrically connected to the pins on the top surface of the substrate by a plurality of wirings. Next, a transparent glass is used to cover the image sensing chip to complete the package of the image sensor. Thus, the pins on the bottom surface of the substrate can be electrically connected to the circuit board in the image sensor after packaging.

In the substrate of the image sensor including ceramic materials, it should be noted that both the selection of the components of the ceramic materials and the control of the working temperature are very important in manufacturing processes.

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If an improper selection or control is made, the substrate may be easily deformed or cracked. Furthermore, since the ceramic substrate cannot be easily cut without any crack, it must be made individually. Therefore, a lot of ceramic substrates cannot be manufactured at the same time, causing the manufacturing costs relatively high.

Moreover, since the pins on the bottom surface of the substrate are used for electrically connecting to the circuit board, the pins have to form a flat contact surface in order not to influence the electrical connection relationship between the substrate and the circuit board. However, the prior art pins are formed into "\subset" shapes on the top and bottom surfaces by pressing. As a result, it is not easy to form a plurality of flat pins, thereby influencing the signal transmission in the image sensor.

In order to solve the above problems, it is necessary to provide a packaging structure and method for an image sensor in which the manufacturing costs can be lowered and the signal transmission of the image sensing chip can be improved.

SUMMARY OF THE INVENTION

It is therefore an object of the invention to provide a packaging structure and method for an image sensor which can be manufactured in large quantities to lower the manufacturing costs.

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Another object of the invention is to provide a packaging structure and method for an image sensor in which the signal transmission distances can be made shorter to perform better signal transmission properties.

To achieve the above objects, a packaging structure of an image sensor includes a substrate, an image sensing chip, a plurality of wirings, and a transparent layer. The substrate includes a plurality of metal sheets, glue for sealing the metal sheets, a first surface, and a second surface. The metal sheets are exposed to the outside via the first surface and the second surface to form first contacts and second contacts, respectively. The image sensing chip is mounted on the substrate. The plurality of bonding pads are formed on the image sensing chip. The plurality of wirings electrically connect the bonding pads of the image sensing chip to the first contacts of the first surface of the substrate, in order to electrically connect the image sensing chip to the substrate. The transparent layer is arranged above the image sensing chip.

Therefore, the second contacts formed on the metal sheets can electrically and smoothly connect to the circuit board. Furthermore, the signal transmission distances between the image sensing chip and the circuit board can be effectively shortened so as to obtain better signal transmission effects. Moreover, since the substrate formed by sealing the metal sheets with the glue can be easily cut, a lot of substrates can be manufactured at the same time by molding so that the manufacturing costs can be lowered.

BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a cross-sectional view showing the packaging structure of the

image sensor according to the invention.

FIG. 2 is a first schematic illustration showing the packaging method of the image sensor according to the invention.

FIG. 3 is a pictorial view showing the substrate of the image sensor according to the invention.

FIG. 4 is a second schematic illustration showing the packaging method of the image sensor according to the invention.

DETAIL DESCRIPTION OF THE INVENTION

Referring to FIG. 1, the packaging structure of the image sensor of the invention includes a substrate 10 made of plastic materials, an image sensing chip 12 located on the substrate 10 and electrically connected thereto, and a transparent layer 14 above the image sensing chip 12. The image sensing chip 12 receives optical signals via the transparent layer 14.

The substrate 10 includes a plurality of metal sheets 16, glue 18 for sealing the metal sheets 16, a first surface 20, and a second surface 22. Referring to FIG. 2, the method for manufacturing the substrate 10 includes the following steps. First, adhere the plurality of metal sheets 16 onto a tape 24. Next, place the tape 24 on a mold (not shown). Then, pour the glue into the mold, the glue can be a plastic material such as epoxy mold compound, BT, FR4, FR5, PPE, or the like. Thereafter, tear the tape 24 to complete the manufacturing processes of the substrate 10, as shown in FIG. 3. At this time, the metal sheets 16 are exposed to

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the outside via both of the first surface 20 and the second surface 22 of the substrate 10, respectively, in order to form first contacts 26 and second contacts 28 on the substrate 10. Alternatively, a projecting edge 30 is formed around the first surface 20 of the substrate 10 in order to form a concavity 32 for containing the image sensing chip 12. The projecting edge 30 can be integrally formed with the substrate 10. Optionally, the projecting edge 30 also can be made of plastic materials, and then, the projecting edge 30 is adhered onto the substrate 10. Thus, the transparent layer 14 can be placed on the projecting edge 30 in order to seal the image sensing chip 12 which can receive optical signals.

The image sensing chip 12 is adhered within the concavity 32 of the first surface 20 of the substrate 10. A plurality of bonding pads 34 are provided on the image sensing chip 12 for electrically connecting to the substrate 10 through a plurality of wirings 15. The bonding pads 34 on the image sensing chip 12 are electrically connected to the first contacts 26 on the first surface 20 of the substrate 10 through the plurality of wirings 15 by way of wiring bonding, respectively. Thus, the image sensing chip 12 is electrically connected to the first contacts 26 on the first surface 20 of the substrate 10, thereby enabling the electrical signals from the image sensing chip 12 to be transmitted to the second contacts 28 of the second surface 22 of the substrate 10. As a result, the substrate 10 can be electrically connected to the circuit board (not shown) via the second contacts 28. Since the metal sheets 16 are very thin, the transmission distances between the first contacts 26 and the second contacts 28 are very short. Consequently, the signals from the image sensing chip 12 can be effectively

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transmitted to the second contacts 28. Furthermore, since the image sensing chip 12 is quite flat and thin, the image sensing chip 12 is capable of effectively and electrically contacting to the circuit board (not shown), when mounting the substrate 10 onto the circuit board.

The transparent layer 14 can be a piece of transparent glass mounted on the projecting edge 30 on the first surface 20 of the substrate 10 for covering the image sensing chip 12. Therefore, the image sensing chip 12 is capable of receiving the optical signals in order to form an image sensor.

Referring to FIG. 4, the projecting edge 30 is adhered onto the substrate 10 in order to form a concavity 32 on the substrate 10 for containing the image sensing chip 12. The transparent layer 14 is adhered onto the projecting edge 30 for receiving optical signals from the image sensing chip 12.

To sum up, the method for packing the image sensor of the invention is described hereinbelow.

15 First, provide a substrate 10 including a plurality of metal sheets 16, glue 18 for sealing the metal sheets 16, a first surface 20, and a second surface 22. At this time, the metal sheets 16 are exposed to the outside via the first surface 20 and the second surface 22 to form first contacts 26 and second contacts 28.

Next, provide a projecting edge 30 on the first surface 20 of the substrate 10 to form a concavity 32 on the substrate 10 for containing the image sensing chip 12.

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Then, place the image sensing chip 12 within the concavity 32 and connect the first contacts 26 of the substrate 10 to the bonding pads 34 on the image sensing chip 12, in order to electrically connect the image sensing chip 12 to the substrate 10. At this time, the signals from the image sensing chip 12 can be transmitted to the second contacts 28 of the substrate 10. Therefore, the second contacts 28 of the substrate 10 can electrically connect to the circuit board (not shown).

Thereafter, adhere the transparent layer 14 onto the projecting edge 30 located on the first surface 20 of the substrate 10. At this time, the transparent layer 14 covers the image sensing chip 12 so that the image sensing chip 12 can receive optical signals. Thus, an image sensor can be formed.

The packaging structure and method of the invention have the following advantages.

- 1. Since the signals from the image sensing chip 12 can be transmitted to the circuit board by the thin metal sheets 16 of the substrate 10, relatively short transmission distances and better signal transmission effects can be obtained.
 - 2. Since the metal sheets 16 of the substrate 10 have smooth surfaces, better electric contacting effects between the substrate 10 and the circuit board can be obtained.
- 3. Since the plastic material of the substrate 10 can be easily cut, a large amount of substrates can be formed by molding. Then, the substrates can be cut

into a plurality of single substrates. As a result, the substrates can be quickly made so that the manufacturing costs can be lowered.

4. Since the plastic material for forming the substrate 10 is cheaper than the ceramic material used in the prior art, the package costs also can be lowered.

While the invention has been described by way of example and in terms of a preferred embodiment, it is to be understood that the invention is not limited to the disclosed embodiment. To the contrary, it is intended to cover various modifications. Therefore, the scope of the appended claims should be accorded the broadest interpretation so as to encompass all such modifications.

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